



PRINTED CIRCUIT BOARDS
INTERCONNECTION CARRIERS

State of the Art: PCB's

Revisio

Datum:

Seite:

PRINTED CIRCUIT BOARDS

01

4.11.2001

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Schematic Key for Multilayer and HDI-Technology Build-Ups

a	b	c	d	e	f	g + h + i
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04 157 FR4 35 L51.35 P18_10

columns and equal kind of positions are separated by "_". Equal prefixes in one column are reduced to one.

04_157_FR4_35_L51.35_p18_10

Layers	in μ	Material	Build-Up	Assembly
Layer-1	35 μ	Copper		A1 B
	100 μ	Prepreg	(100 μ PrePreg-Type: 2125)	
	180 μ	Prepreg	(180 μ PrePreg-Type: 7628)	
	180 μ	Prepreg		
Layer-2	35 μ	Copper		
	510 μ	L-FR4		
Layer-3	35 μ	Copper		
	180 μ	Prepreg		
	180 μ	Prepreg		
	100 μ	Prepreg		
Layer-99	35 μ	Copper		

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