



PRINTED CIRCUIT BOARDS
INTERCONNECTION CARRIERS

State of the Art: PCB's

Revisio

Datum:

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01

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Schematic Key for Multilayer and HDI-Technology Build-Ups

a	b	c	d	e	f	g + h + i
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04 166 FR4 55 L108.35 P10

columns and equal kind of positions are separated by "_". Equal prefixes in one column are reduced to one.

04_166_FR4_55_L108.35_p10

Layers	in μ	Material	Build-Up	Assembly
Layer-1	55 μ	Copper		
	100 μ	Prepreg		
	100 μ	Prepreg		
Layer-2	35 μ	Copper		
	1080 μ	L-FR4		
Layer-3	35 μ	Copper		
	100 μ	Prepreg		
	100 μ	Prepreg		
Layer-99	55 μ	Copper		

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