PRINTED CIRCUIT BOARDS

INTERCONNECTION CARRIERS

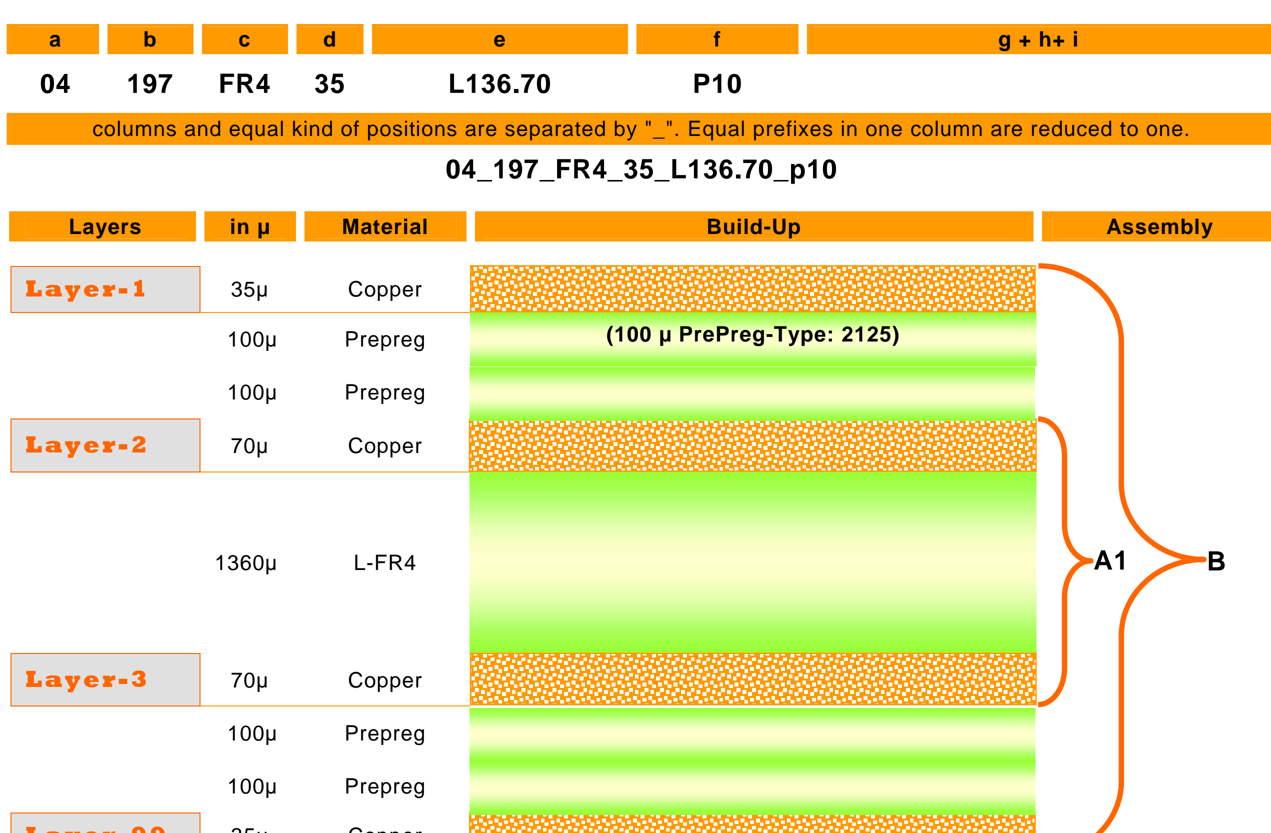
State of the Art: PCB's	*Revisio*	*Datum:*	*Seite:*
PRINTED CIRCUIT BOARDS	**01**	**4.11.2001**	**1/1**

Schematic Key for Multilayer and HDI-Technology Build-Ups

a	b	c	d	e	f	g + h+ i
04	197	FR4	35	L136.70	P10	

columns and equal kind of positions are separated by "_". Equal prefixes in one column are reduced to one.

04_197_FR4_35_L136.70_p10



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